REMARKS/ARGUMENTS

In view of the amendments and remarks herein, favorable reconsideration and allowance of this application are respectfully requested. By this Amendment, claim 1 has been amended. Claims 1-23 are pending for further examination.

Claims 1-10, 12, 15-16, 18, 21 and 23 have been rejected under 35 USC 102(e) as being anticipated by Chen. In addition, claims 11, 13, 14, 17, 19, 20 and 22 have been rejected under 35 USC 103 as being obvious over Chen in view of Nally and Nakamura.

For at least the following reasons, Applicant respectfully submits that amended claims 1 and the remaining claims are not anticipated by or rendered obvious by the cited references. Thus, reconsideration and withdrawal of this rejection are respectfully requested.

Claim 1 has been amended herein to recite a graphics system, including: a main processor; a graphics coprocessor having an embedded frame buffer; and a copy pipeline on said graphics coprocessor which transfers data from the embedded frame buffer to an external image storage location; wherein the copy pipeline converts the data from one format to another format after reading the data from the embedded frame buffer and during transfer of the data to the external image storage location. Applicant respectfully submits that this combination of features is not disclosed in Chen. For example, the Examiner has cited column 4, lines 48-67 and column 5, lines 1-12 as disclosing the feature of using a copy pipeline to transfer the data from an embedded frame buffer to an external memory. However, the disclosures relied on by the Examiner only disclose

FOULADI et al. Appl. No. 09/722,663 February 25, 2004

copying data to different locations within the same chip, rather than from an embedded frame buffer on a graphics co-processor to an external memory location, such as main memory of the graphics system. With respect to converting the data from one format to another during the copy out operation, the Examiner has relied on the same disclosure cited above and the disclosure at col. 2, lines 43-49. However, the conversion of data identified by the examiner in Chen is done by the rasterizer chip 16, which provides some formatting of the data from the frame buffer prior to sending it to the monitor 20. None of the teachings of Chen cited by the Examiner teach the copy out feature of claim 1, as amended herein, in which the copy pipeline converts the data from one format to another format after reading the data from the embedded frame buffer of a graphics co-processor and during transfer of the data to the external image storage location, such as main memory. Thus, Applicant respectfully submits that amended claim 1 is not anticipated by Chen. Moreover, the remaining reference of record fail to make up for the deficiencies of Chen. Thus, allowance of amended claim 1 and dependent claims 2-11 are respectfully requested.

Independent claim 12 recites a method of transferring data from a graphics chip to an external image storage destination, including: storing image data in an embedded frame buffer of the graphics chip; initiating a copy out operation for transferring data from the embedded frame buffer to the external image storage destination; converting the data from one format to another format during the copy out operation; and writing the converted data to the external image storage destination. Thus, claim 12 as presently

FOULADI et al. Appl. No. 09/722,663 February 25, 2004

presented requires that the copy out operation transfer data from an embedded frame

buffer of a graphics chip to an external storage and that the conversion occur during the

copy out operation. Thus, Applicant respectfully submits that claim 12 and the remaining

dependent claims are allowable over the prior art of record for substantially the same

reasons set forth above with respect to claim 1.

In view of the foregoing amendments and remarks, Applicant respectfully submits

that all of the pending claims patentably distinguish the prior art of record and are in

condition for allowance. Thus, withdrawal of the rejections and passage of this case to

issuance at an early date are earnestly solicited.

Should the Examine have any questions regarding this response, or deem that any

further issues need to be addressed prior to allowance, the Examiner is invited to call the

undersigned attorney at the phone number below.

Respectfully submitted,

NIXON & VANDERHYE P.C.

Reg. No. 35,329

JSP:mg

1100 North Glebe Road, 8th Floor

Arlington, VA 22201-4714

Telephone: (703) 816-4000

Facsimile: (703) 816-4100

- 8 -

819741